

IN THE UNITED STATES PATENT OFFICE

266
#9
39
(0287)

Applicant: Master et al.)
Serial No.: 09/997,530)
Filing Date: November 30, 2001)
Title: Apparatus, System and Method)
For Configuration of Adaptive)
Integrated Circuitry Having Fixed)
Application Specific)
Computational Elements)

PATENT APPLICATION
Examiner: Not Yet Assigned
Art Unit: 2661
Dkt. # 0002240.0009 **RECEIVED**
MAY 13 2003
Technology Center 2600

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

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Sir:

Please find enclosed form PTO/SB/08A along with a copy of the two (2) cited references. These references were cited in the PCT International Search Report, a copy of which is also enclosed, for PCT Application No. PCT/US02/37014, which is the equivalent of this pending U.S. application.

If any fees are required in connection with this filing, the Commissioner is authorized to charge or credit Deposit Account No. 501,262 any amount that may be required. Duplicate copies of this letter are enclosed.

Respectfully submitted,

Nancy R. Gamburd
Registration No. 38,147

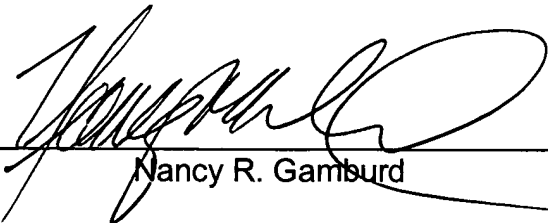
Dated: May 8, 2003

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, prepaid postage, in an envelope addressed to Mail Stop DD, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on May 8, 2003.



Nancy R. Gumbard

